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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NO: 10007836	FILING DATE 11/05/2001	CLASS 712	SUBCLASS	GAU 2154	EXAMINER FOLLANSBEE, J
**APPLICANTS: Moyer William; Arends John; Scott Jeffrey; <i>W. Leg, Pwiz</i>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/05,260 06/30/2000 PAT 6,327,647					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials			ATTORNEY DOCKET NO SC90674A CD1		
TITLE : Method and apparatus for interfacing a processor to a coprocessor <small>U.S. DEPT. OF COMM./PAT. & TM-PTO-231 (Rev. 12-94)</small>					

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NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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